SILICON CARBIDE (SIC) SEMICONDUCTORS FOR (X)EV ARE GETTING CLOSER TO REALITY

Dr. Andre Christmann, Infineon Technologies

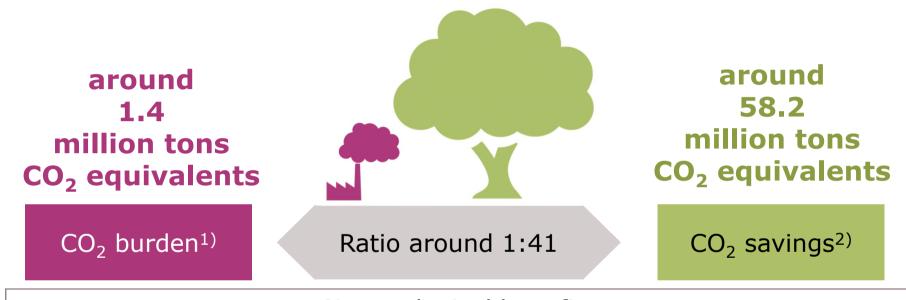




Content

| | Silicon Carbide at Infineon |
|---|-----------------------------------|
| 2 | Silicon Carbide in automotive |
| | Challenges |
| | The system benefits of SiC |
| 3 | Why is SiC now closer to Reality? |
| 4 | Next generation of power module |
| | |

Emission Reduction enabled by our products and solutions

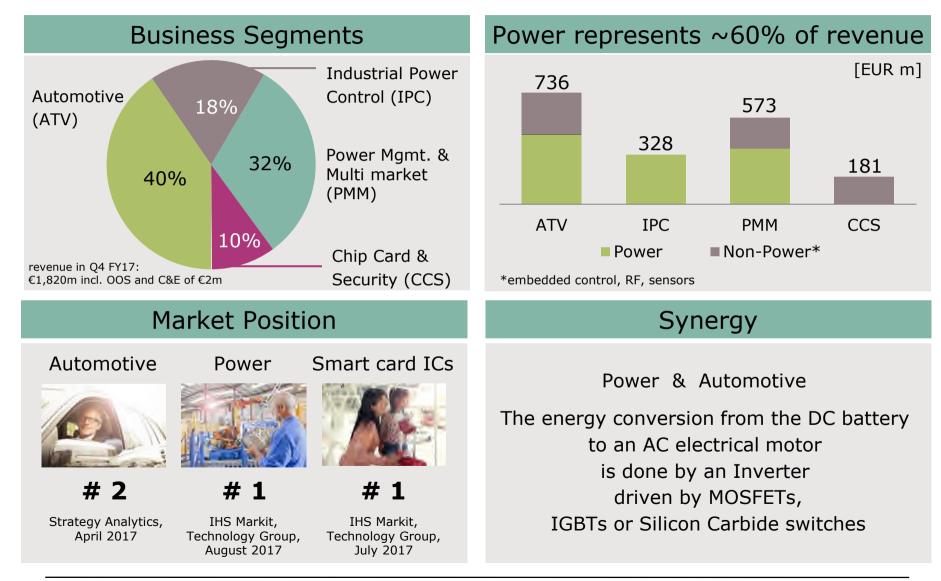


Net ecological benefit: CO₂ emissions reduction of more than 56 million tons

1) This figure considers manufacturing, transportation, function cars, flights, materials, chemicals, water/wastewater, direct emissions, energy consumption, waste, etc. and is based on internally collected data and externally available conversion factors. All data relate to the 2017 fiscal year.

2) This figure is based on internally established criteria, which are explained in the explanatory notes. The figure relates to the calendar year 2016 and considers the following fields of application: automotive, LED, induction cookers, PC power supply, renewable energy (wind, photovoltaic), mobile phones' chargers as well as drives. CO₂ savings are calculated on the basis of potential savings of technologies in which semiconductors are used. The CO₂ savings are allocated on the basis of the Infineon market share, semiconductor content and lifetime of technologies concerned, based on internal and external experts' estimations. Despite the fact that CO₂ footprint calculations are subject to imprecision due to the complex issues involved, the results are nevertheless clear.

Infineon at a glance



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Product range



Automotive (ATV)

- 32-bit automotive microcontrollers for powertrain, safety and driver assistance systems
- Discrete power semiconductors
- > IGBT modules
- > Industrial microcontrollers
- Magnetic and pressure sensors
- Power ICs
- Radar
- Transceiver (CAN, LIN, Ethernet, Flex Ray^{™*})
- Voltage regulators



Industrial Power Control (IPC)

- Bare die business
- > Discrete IGBTs
- > Driver ICs
- IGBT modules (highpower, mediumpower, low-power)
- IGBT module solutions incl. IGBT stacks



Power Management & Multimarket (PMM)

- Control ICs
- Customized chips (ASICs)
- Discrete low-voltage and high-voltage power transistors
- > GPS low-noise amplifier
- Low-voltage and highvoltage driver ICs
- MEMS and ASICs for silicon microphones
- RF antenna switches
- RF power transistors
- TVS (transient voltage suppressor) diode



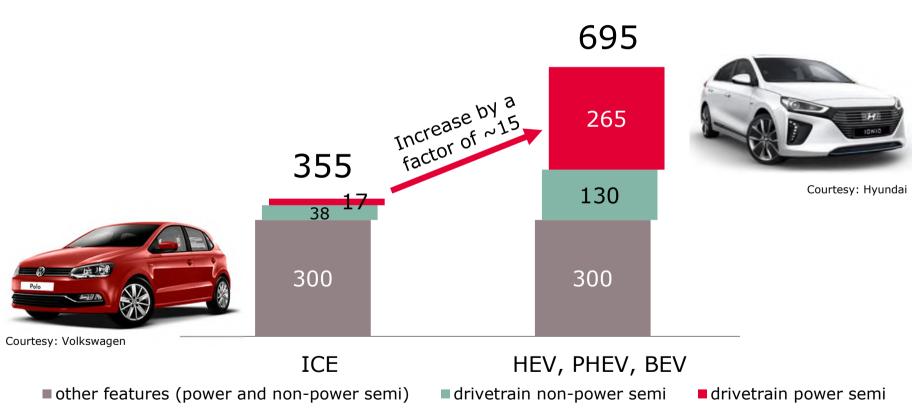
Chip Card & Security (CCS)

- Contact-based security controllers
- Contactless security controllers
- Dual-interface security controllers (contactbased and contactless)

With the transition from ICE to xEV the power semi content in powertrain increases by $\sim 15x$

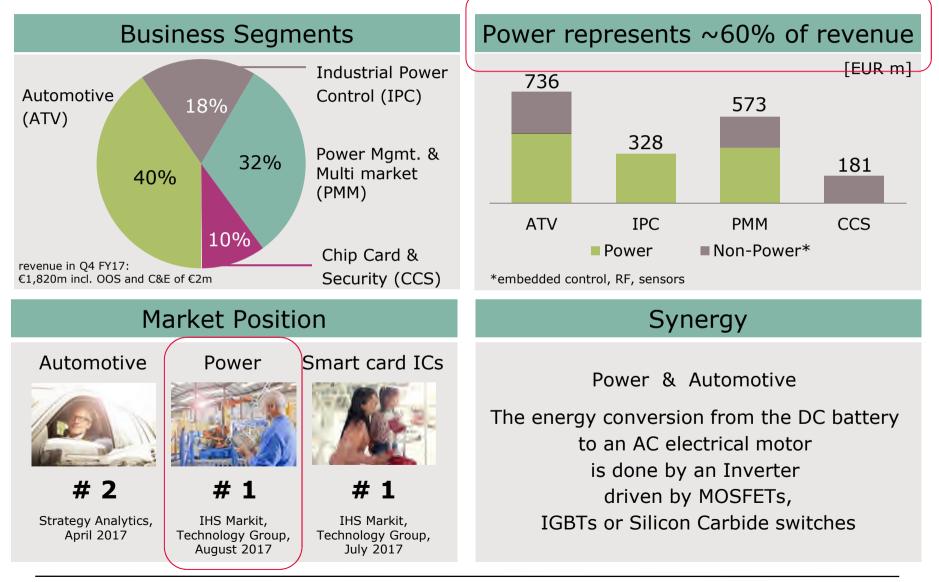


[USD]



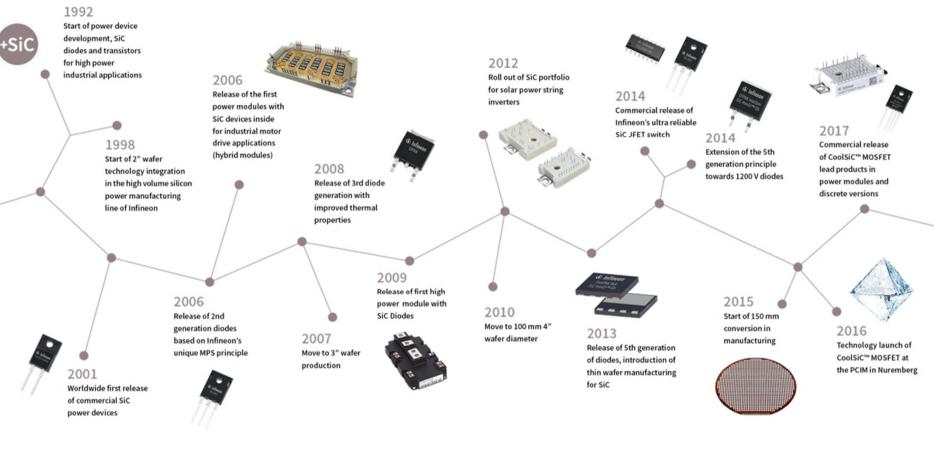
Source: Strategy Analytics, "Automotive Semiconductor Content", May 2017; Infineon

Infineon at a glance # 1 in Power



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Infineon builds up on **25 years of experience** in SiC technology

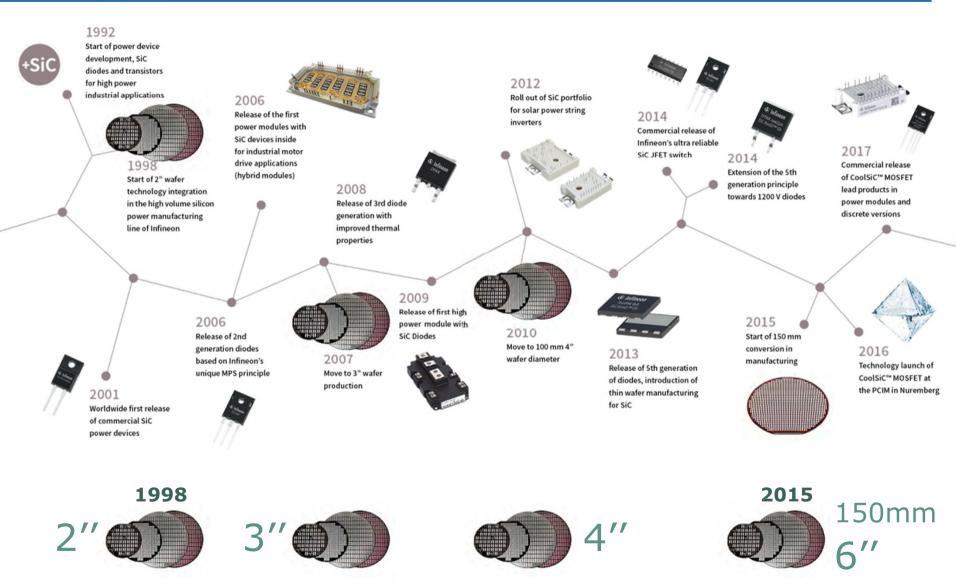


1 in Power



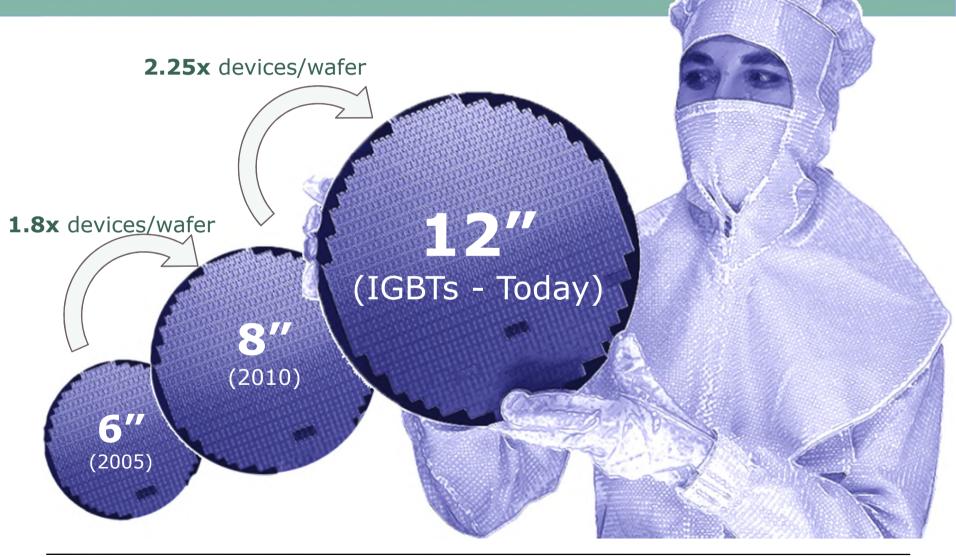
Infineon is one of the few players with a long track record in Silicon & Silicon Carbide

Infineon builds up on 25 years of experience Increase of wafer size over time

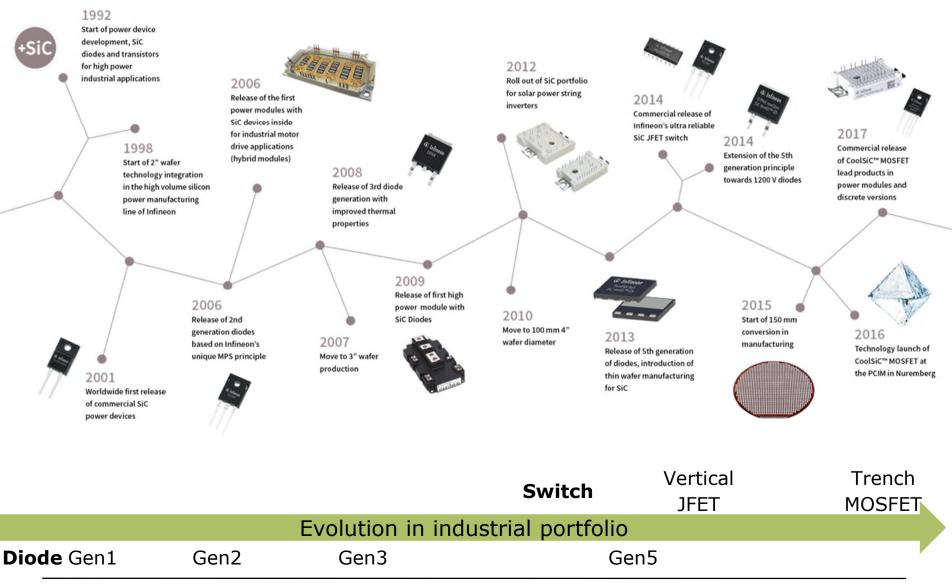


Lower the cost Economies of Scale for IGBTs

The world's first 300mm High Power Silicon device FAB



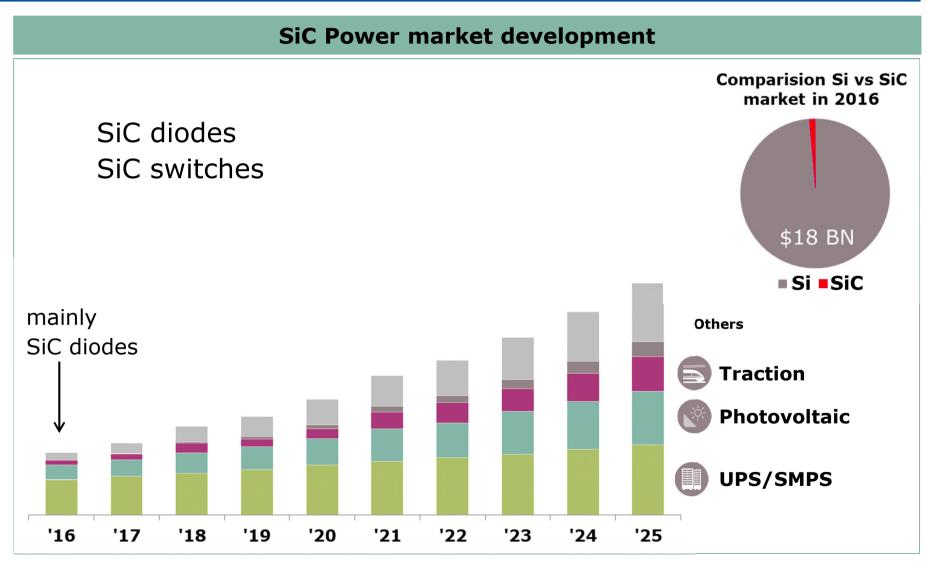
Infineon builds up on 25 years of experience Increase of package/product portfolio



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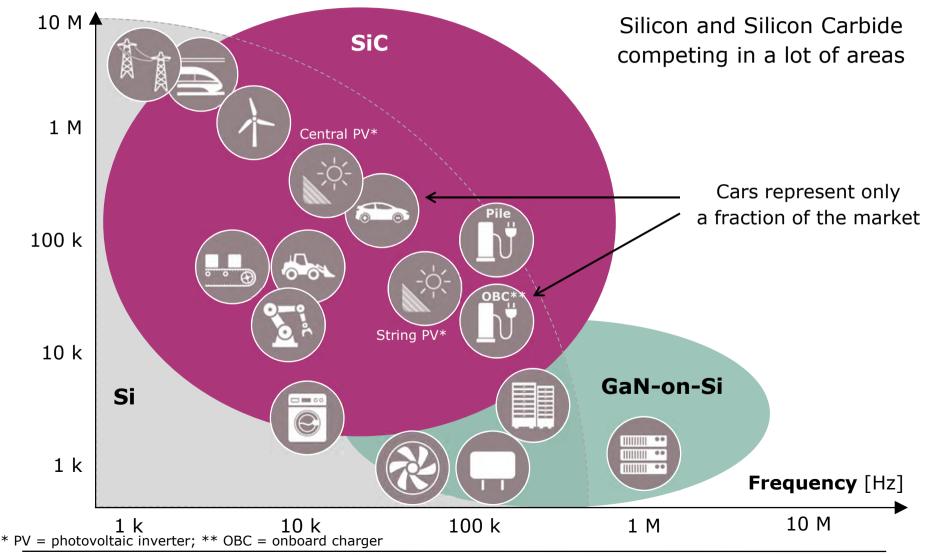
We expect extraordinary growth for SiC, more applications will start the adoption



Sources: IHS Markit, "World Market for SiC and GaN Power Semiconductors", Feb 2016

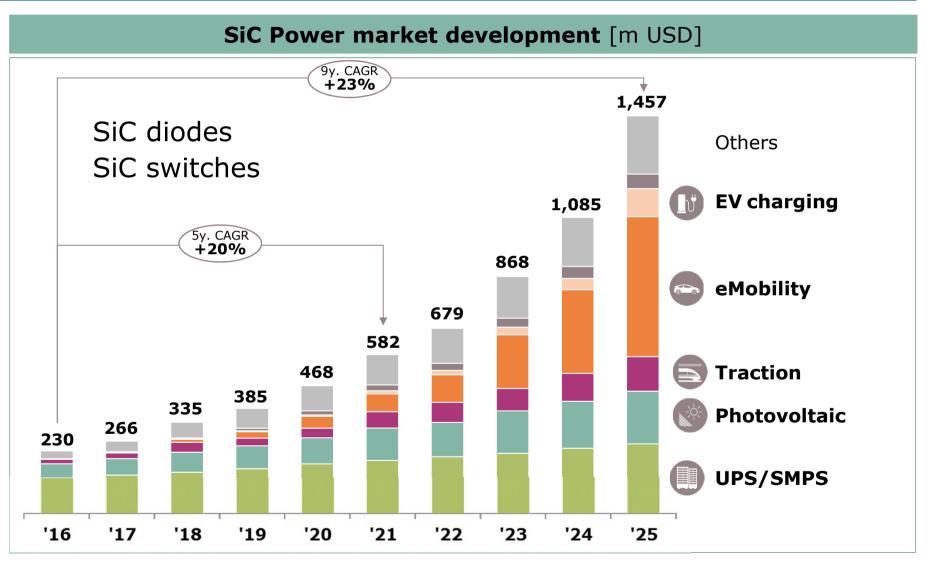
SiC and GaN enable higher efficiency through faster switching at lower losses than Si

Power [W]



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We expect extraordinary growth for SiC, > 20% Compound Annual Growth Rate



Sources: IHS Markit, "World Market for SiC and GaN Power Semiconductors", Feb 2016

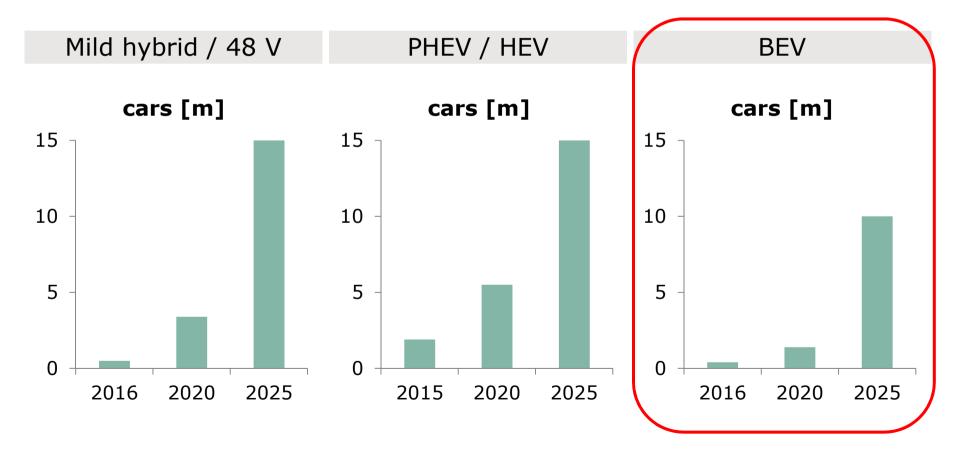
We expect extraordinary growth for SiC in eMobility and charging

SiC Power market development



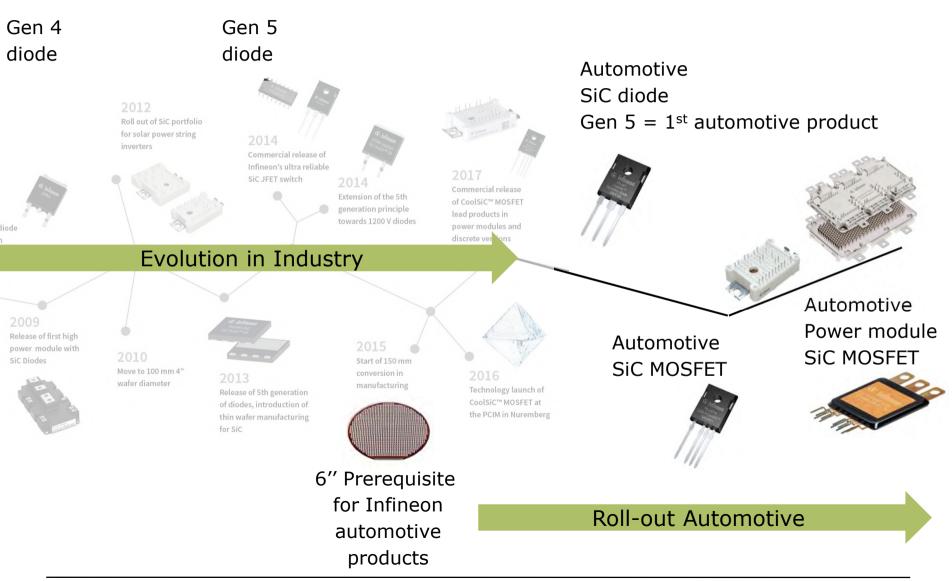
Sources: IHS Markit, "World Market for SiC and GaN Power Semiconductors", Feb 2016

What is the Growth Driver ?



Source: IHS Automotive, "Alternative Propulsion Forecast", January 2017

Infineon builds up on 25 years of experience Evolution: Entering the automotive Market



Brief recapitulation

- Trends
 - Growing market share of SiC products
 - Steep increase of SiC in automotive applications
 - 10 Million BEV in 2025



- 150 mm Wafer in SiC production line established
- First automotive qualified products in **2018**
- SiC MOSFET design based on Trench Technology

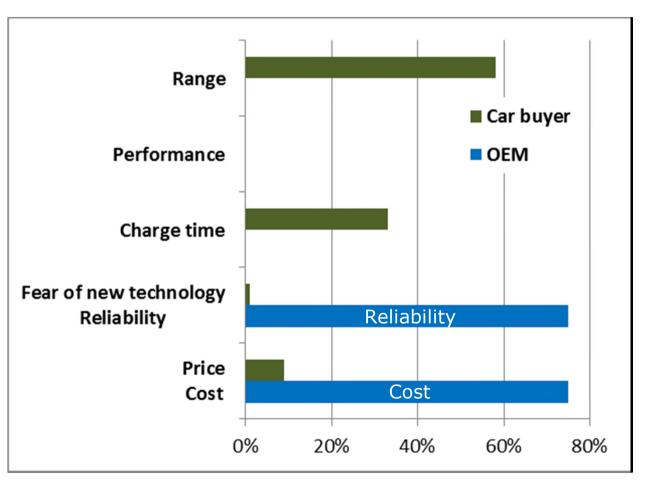
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Recent Infineon public poll on xEV adoption

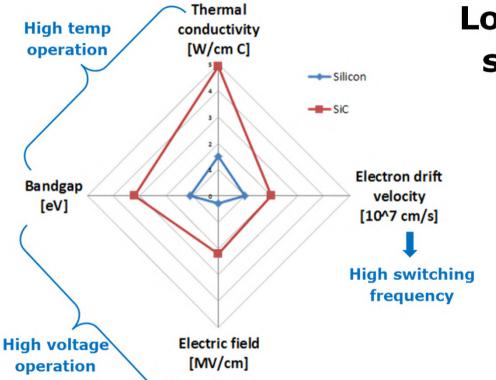
Car buyer: What are the barriers to xEV adoption?

OEMs: What are the barriers to SiC adoption?



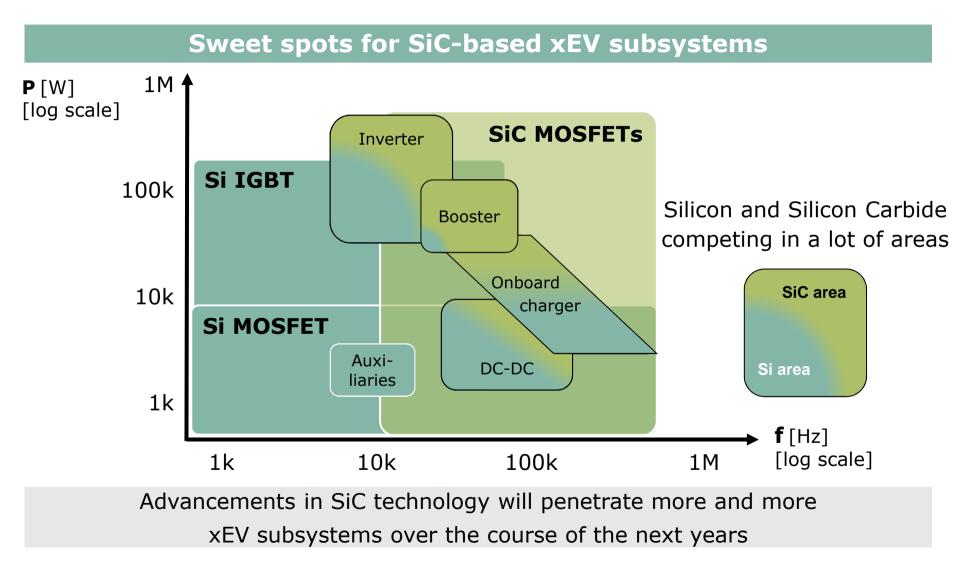
Wide Band Gap could be a game changer Comparison of physical properties

> Advantages of WBG materials



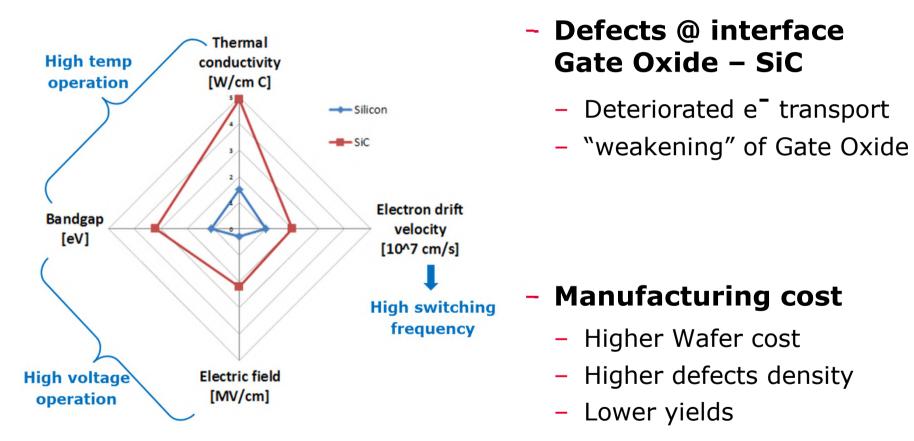
Low losses even at high switching frequencies & high voltages

Technology fit: SiC is the option of choice for most demanding xEV subsystems



Wide Band Gap could be a game changer Superior but comes with some disadvantages

> Advantages of WBG materials



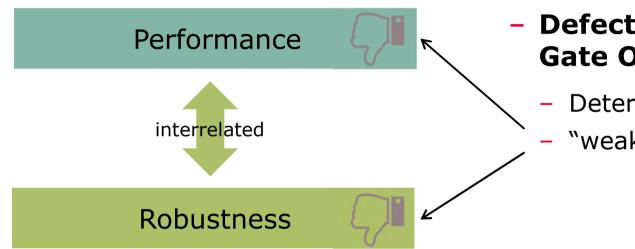
>

Disadvantages in SiC

GOX: Gate Oxide – thin insulating layer for control of the device, most sensitive part of the device SAE INTERNATIONAL
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Silicon Carbide - Oxide Robustness Reduced Robustness & reduced Performance

A thick oxide reduces the performance



Disadvantages in SiC

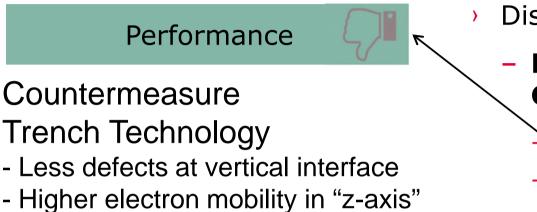
- Defects @ interface
 Gate Oxide SiC
 - Deteriorated e⁻ transport
 - "weakening" of Gate Oxide

Countermeasure thick oxide

- lower failure rate for extrinsic defects
- lower electrical fields across GOX
- efficient screening (higher voltages)

GOX: Gate Oxide – thin insulating layer for control of the device, most sensitive part of the device SAE INTERNATIONAL
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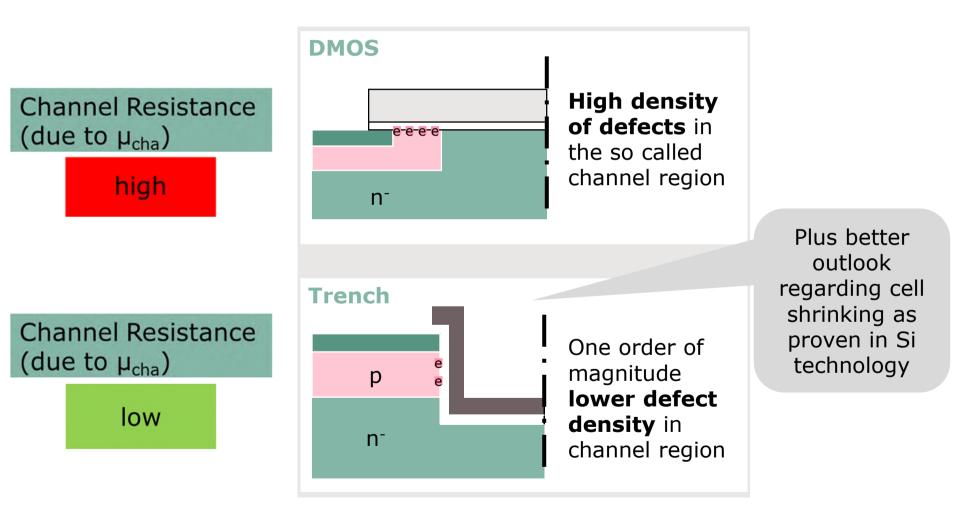
Silicon Carbide - Oxide Robustness Reduced Performance



Disadvantages in SiC

- Defects @ interface
 Gate Oxide SiC
 - Deteriorated e transport
 - "weakening" of Gate Oxide

Reliability – Oxide Robustness Planar MOS vs. Trench MOS



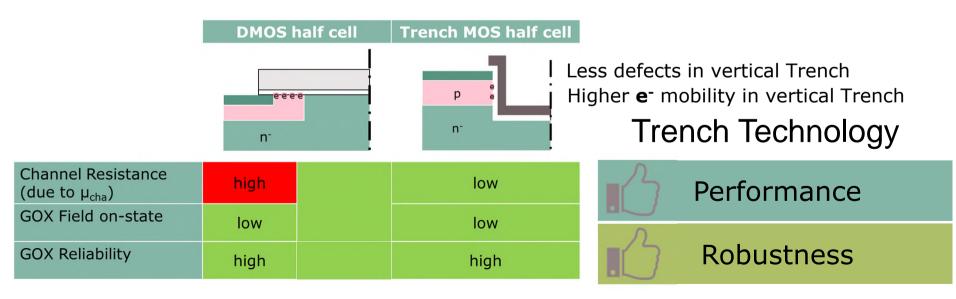
Silicon Carbide Planar MOS vs. Trench MOS



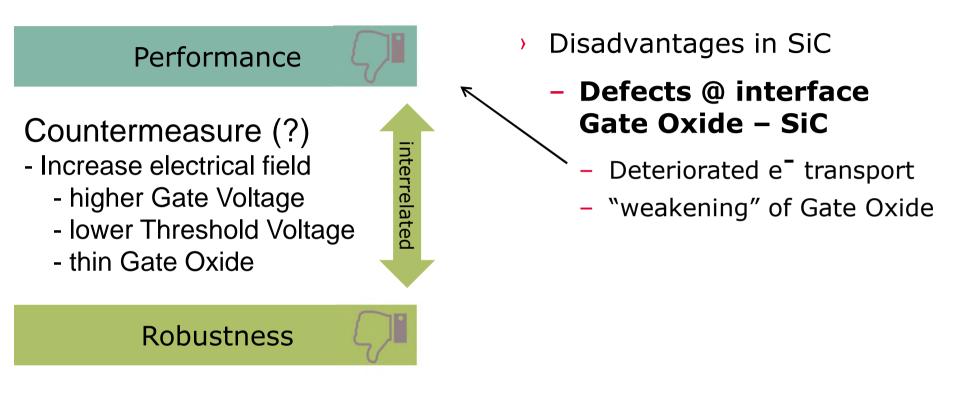
Countermeasure Trench Technology

- Less defects at vertical interface
- Higher electron mobility in "z-axis"

- Disadvantages in SiC
 - Defects @ interface
 Gate Oxide SiC
 - Deteriorated e transport
 - "weakening" of Gate Oxide



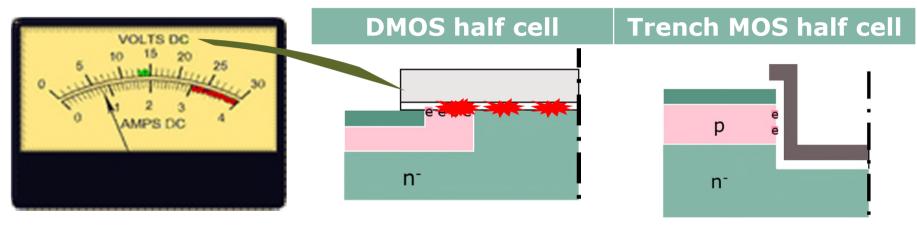
Reliability – Oxide Robustness Planar MOS vs. Trench MOS



SiC MOSFET concepts Performance / Robustness trade-off

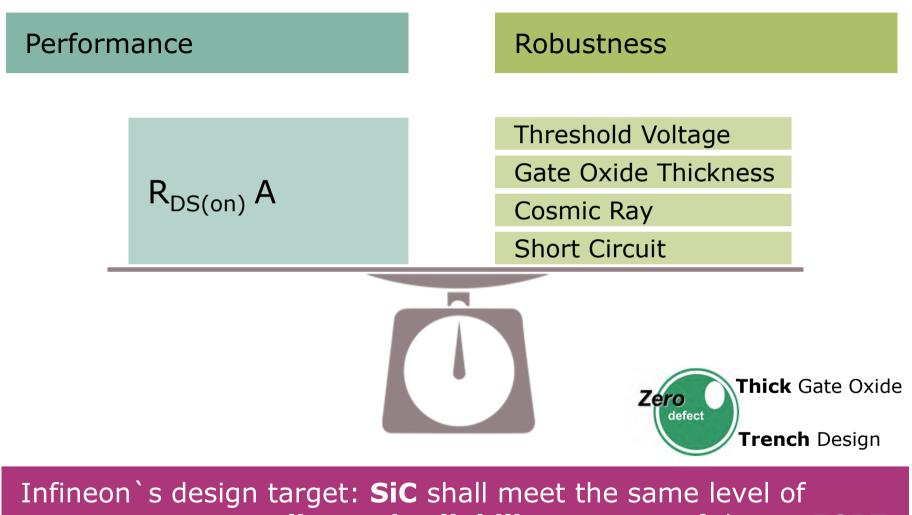
Countermeasure (?): Increase electrical field

- higher Gate Voltage



| Channel Resistance (due to μ_{cha}) | low | low |
|---|------|------|
| GOX Field on-state | high | low |
| GOX Reliability | low | high |

A balanced approach is needed Performance / Robustness trade-off

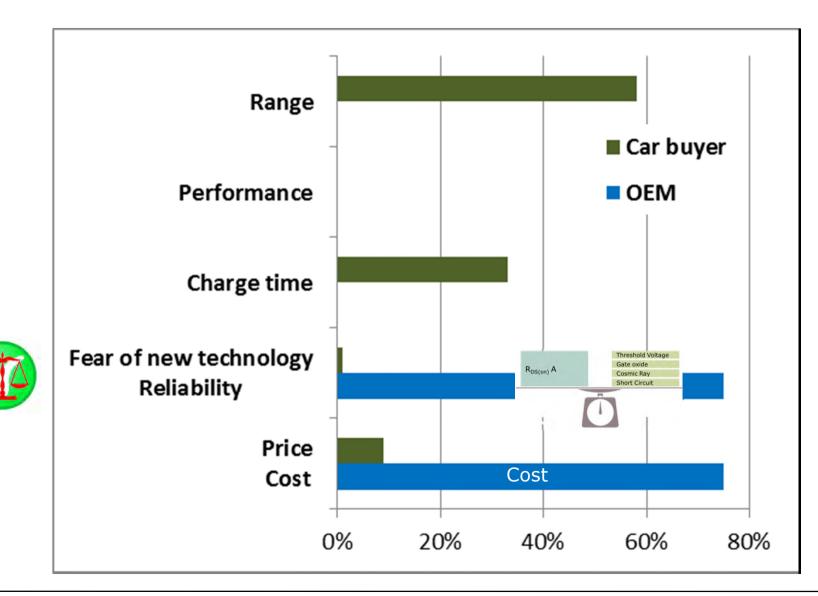


quality and reliability as state of the art IGBT

Recapitulation

- **Reliability** Challenge: Defect density and Gate Oxide Robustness
 - Wafer quality improving significantly
 - Infineon`s SiC MOSFET design based on Trench Technology and long time experience enables the reduction of fit rates

SiC addresses several obstacles Reliability needs balanced approach

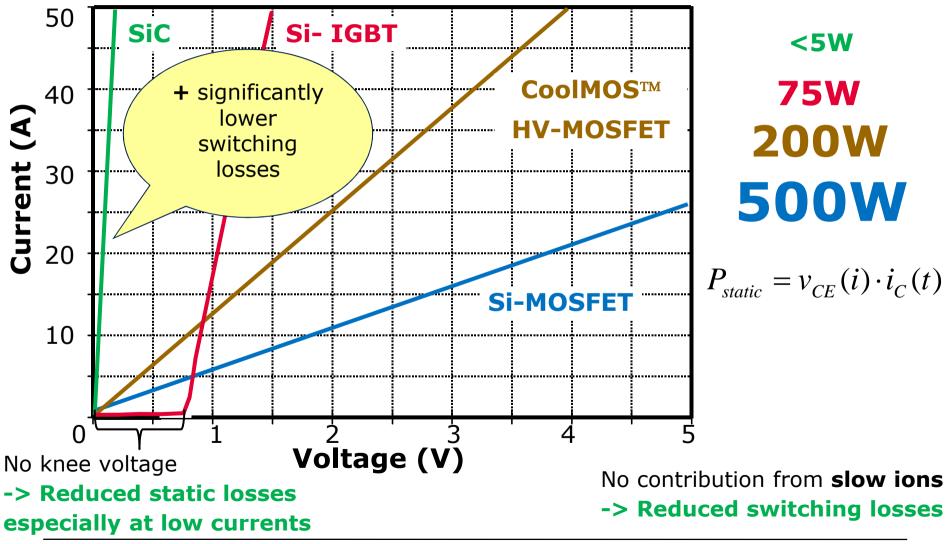


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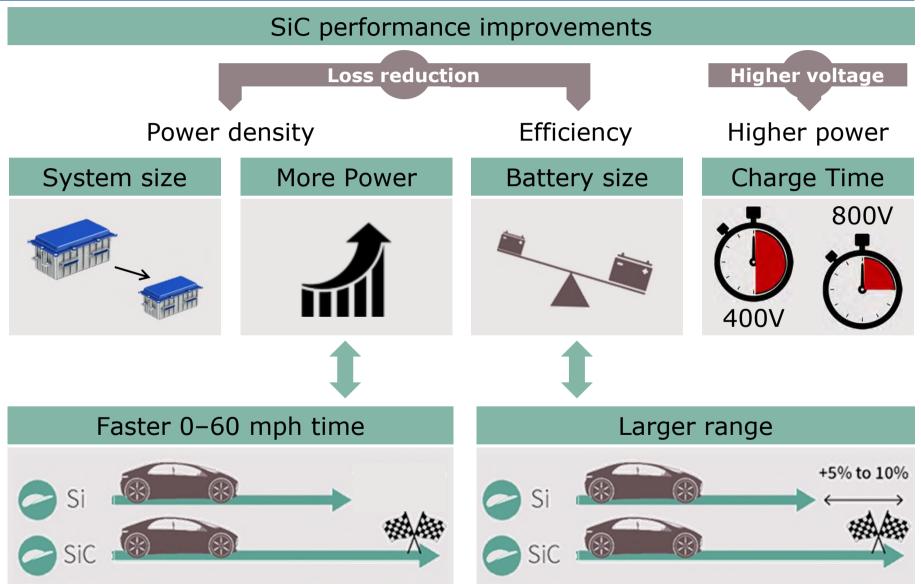
Benefits of SiC MOSFET Reduction of dynamic AND static losses

Power losses for a 1cm² semiconductor at 1000V and 50A



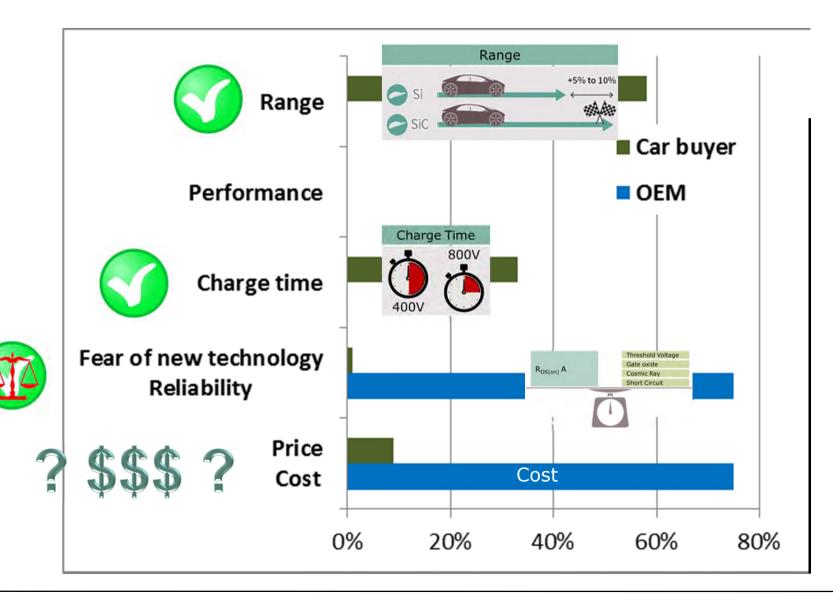
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Power density and efficiency in Traction Inverters are drivers for SiC in automotive

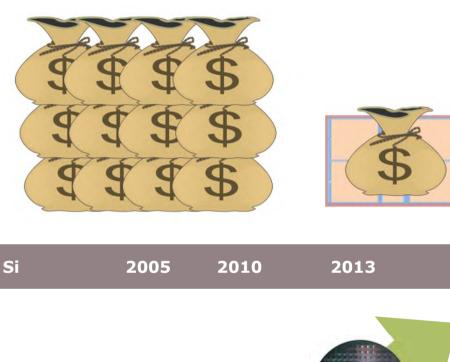


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SiC addresses several concerns Range and charge time improvements



Wide Band Gap could be a game changer SiC is an expensive material



4" 6" 8" I2"

SiC is an expensive material, but

\$ / Amp ratio is decreasing fast

- Higher demand -> lower cost
- Better Wafers -> lower defect density
- Wafer size increase -> lower cost

Manufacturing cost

- Higher Wafer cost
- Higher defects density
- Lower yields

2010

2015

SiC

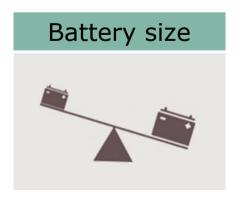
Efficiency improvements of SiC can save system costs at car level

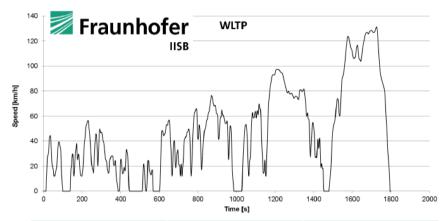
Drive cycle study shows

 A SiC inverter shows 2/3rd less losses.

Efficiency Improvement

 For a given autonomy, this leads to savings in the high voltage battery.

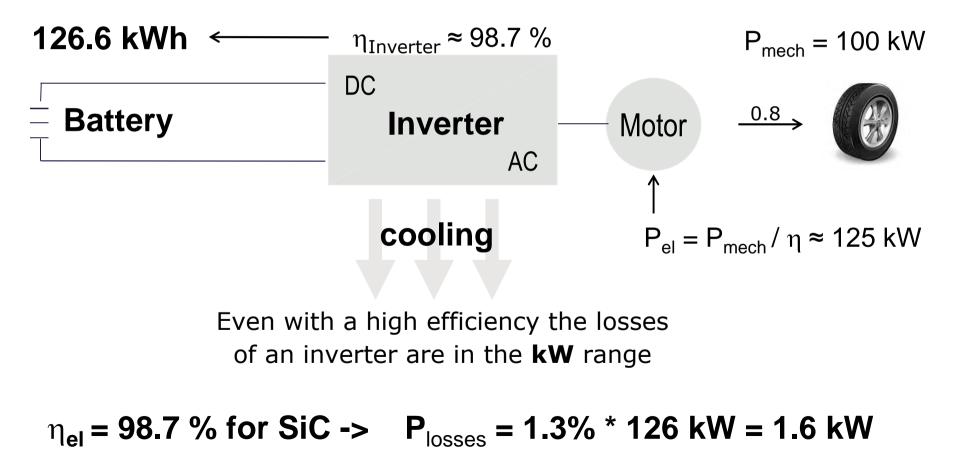




| Driving Cycle | Si based Inverter | SiC based Inverter |
|--------------------|----------------------|------------------------------|
| Artemis Urban | 94.6% | 98.1% |
| Artemis Road | 97.2% | 99.0% |
| Artemis Highway | 98.2% | 99.3% |
| NEDC | 96.3% | 98.7% |
| WLTP | 96.8% | 98.9% |

Scenario:

Driving a vehicle with 100kW "at the wheel"



Efficiency improvement by SiC Battery reduction by ~5% possible

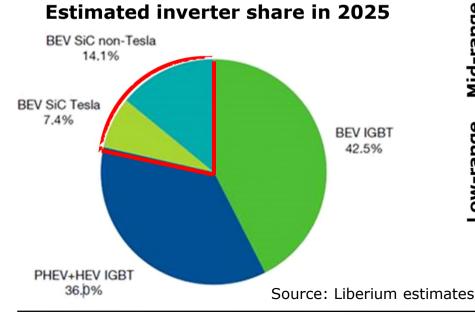
| | | D | rivin | g | | |
|-------------------|----------------------|------------------------|-------|-------------------------------------|---------|----------------------------|
| Battery output | | Inverter Efficiency | | Motor Transmission Efficiency | @ Whee | el |
| 129.8 kW | h | Si 0.963 | | 0.8 | 100 kWł | ′h |
| 126.6 kW | h s | SiC 0.987 | | 0.8 | 100 kWł | Th Effect of 2.4% better |
| Battery input | | | | | | Inverter Efficie |
| 38.5 kWh | | Si 0.963 | | 0.8 | 50 kWh | 1 |
| 39.5 kWh | | SiC 0.987 | | 0.8 | 50 kWh | ~ 5% lower |
| | | Re | cuper | ration | | Battery Capacity |
| Driving Cycle | Si based Inverter | SiC based | | Battery Balance | | Needed Battery Capacity |
| o, cic | | Inverter | | 129.8 kWh – 38. | 5 kWh | Si 91.3 kWh |
| NEDC | 96.3% | 98.7% | | 126.6 kWh – 39. | 5 kWh | SiC 87.2 kWh |

Effect of 4% better rter Efficiency

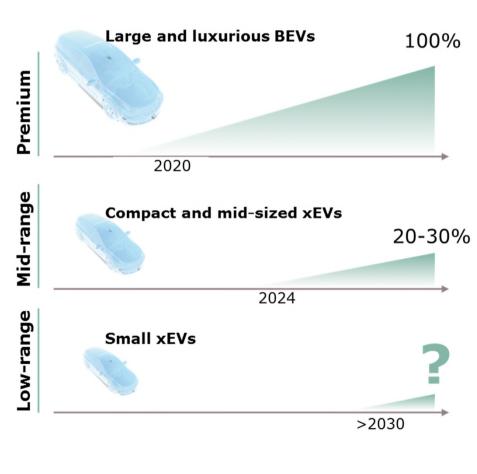
Penetration of SiC into xEV market High benefit for vehicles with large batteries

Efficiency Improvement

- A SiC inverter shows 2/3rd less losses.
- For a given autonomy, this leads to savings in the high voltage battery.



Highest benefit for high voltage systems and vehicles with large batteries expected



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Recapitulation

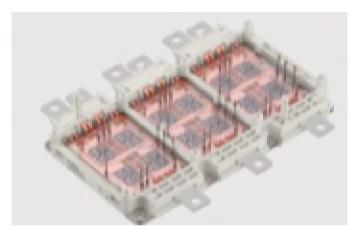
- Reliability Challenge: Defect density and Gate Oxide Robustness
 - Wafer quality improving significantly
 - Infineon`s SiC MOSFET design based on Trench Technology and long time experience enables the reduction of fit rates
- Cost Challenge
 - High demand of SiC Wafers **lowers Wafer cost**
 - Economy of scale: 150 mm Wafer in SiC production line
 - Lower defect density
 - improves yield
 - enables larger die sizes

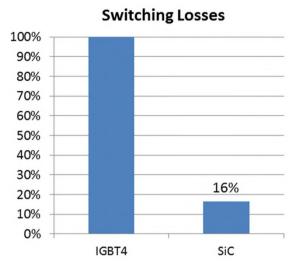
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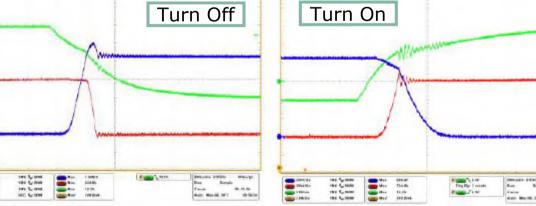
Inverter Module HybridPACK[™] Drive with CoolSiC[™]

Comparison of 1200V SiC and Si (IGBT4)

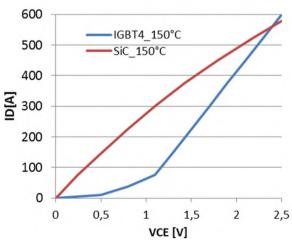




Smooth switching for drop in replacement Turn Off Turn On

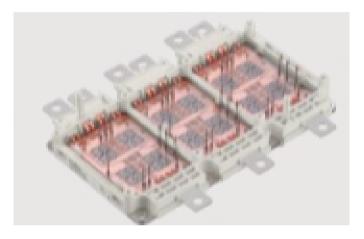


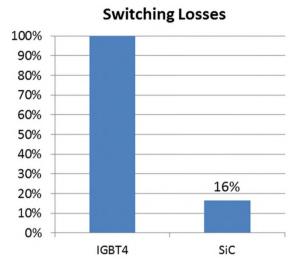


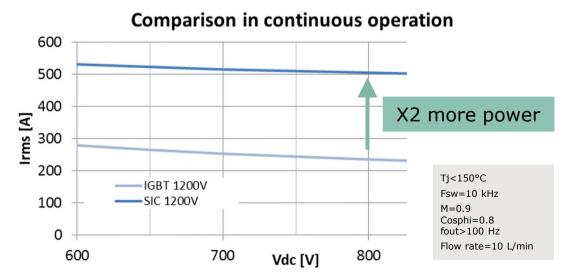


Inverter Module HybridPACK[™] Drive with CoolSiC[™]

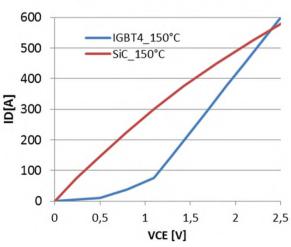
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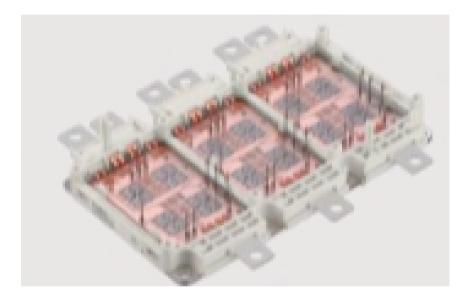




Static Characteristic



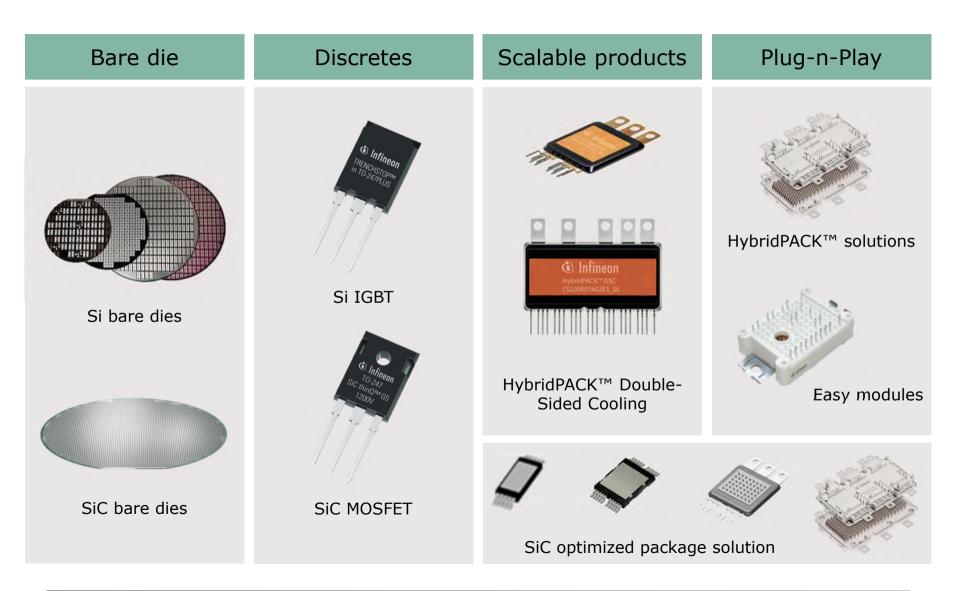
HybridPACK[™] Drive with CoolSiC[™] SiC doubles power density



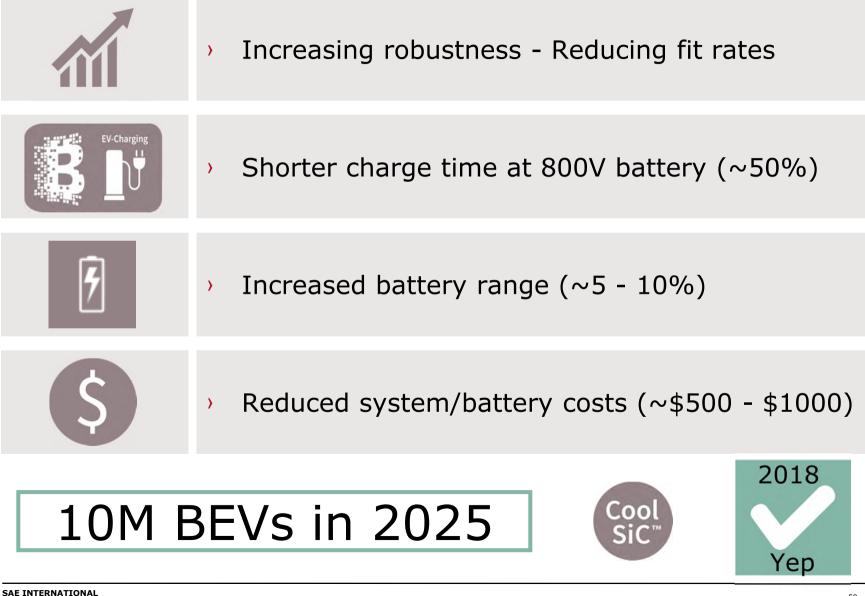
300kW (500A - 800V) SiC inverter module Drop in replacement to Si modules

Power density almost doubled

Infineon has unparalleled package expertise to match OEM and Tier-1 needs



Silicon Carbide (SiC) Semiconductors for xEV are getting closer to Reality



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Part of your life. Part of tomorrow.

